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PATENT
Docket No.: ACT-233

#19

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2825

Examiner: A.M. Thomson

Serial No. 09/069,054

Filed: April 28, 1998

#19/Response
COPY

In re Application of: Chan et al.

For: DEDICATED INTERFACE ARCHITECTURE FOR A HYBRID INTEGRATED CIRCUIT

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Director of Patents, Box DAC, Washington, D.C. 20231 on 10/11/01. Signed, John M. D. D. M.
Name

RESPONSE TO OFFICE ACTION

Director of Patents
Box DAC
Washington, D.C. 20231

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OFFICE OF PETITIONS

Dear Sir:

In response to the Office Action dated September 7, 2000, kindly amend the above-identified application as follows.

In the Drawings

Submitted herewith for the Examiner's approval are proposed amendments to drawing FIGS. 1A-1F, 2 and 3. The proposed amendments to the drawing figures are indicated thereon in red ink.

The 35 U.S.C. § 102 Rejection

Claims 1-3, 7-9, and 11-19 stand rejected under 35 U.S.C. § 102(b) as being allegedly anticipated by Tavana et al. (U.S. Patent No. 5,825,202, hereinafter "202"). This rejection is respectfully traversed.

The Examiner asserts that '202 discloses an integrated circuit with Field